

## ABSTRACT

A method and associated apparatus is provided for improving the  
5 performance of a high speed memory bus by substantially eliminating bus  
reflections caused by electrical stubs. The stubs are substantially eliminated  
by connecting system components in a substantially stubless configuration  
using a looping bus for continuing the looping bus through each device.  
The invention also provides an interface circuit that enables data  
10 communications between devices of different technologies. The interface  
circuit connects to the looping data bus and includes a circuit for providing  
voltage level, encoding type, and data rate conversions for data received  
from the looping data bus and intended for use on a second data bus  
connected to the interface circuit.

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